

FIG.1

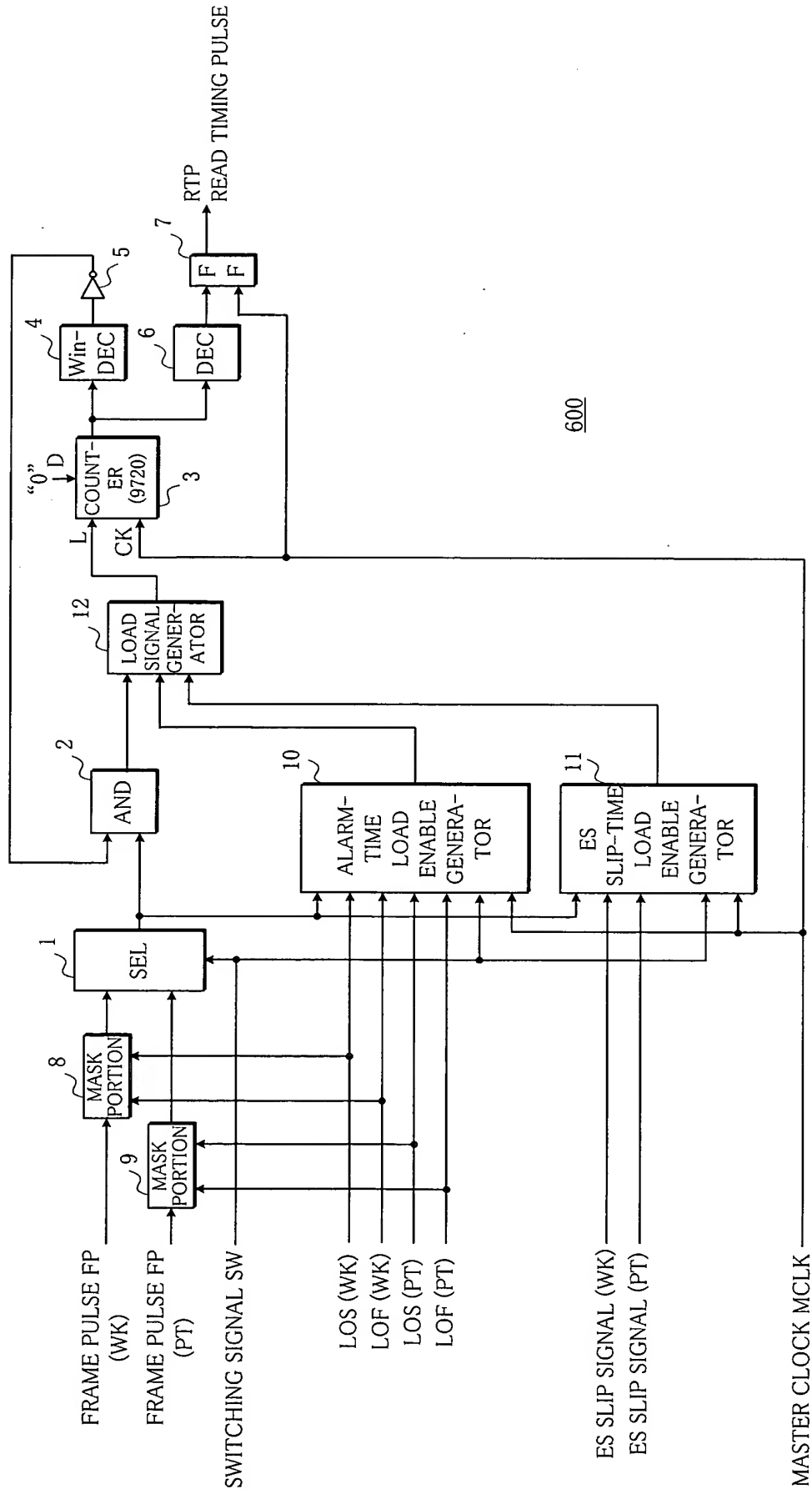


FIG.2

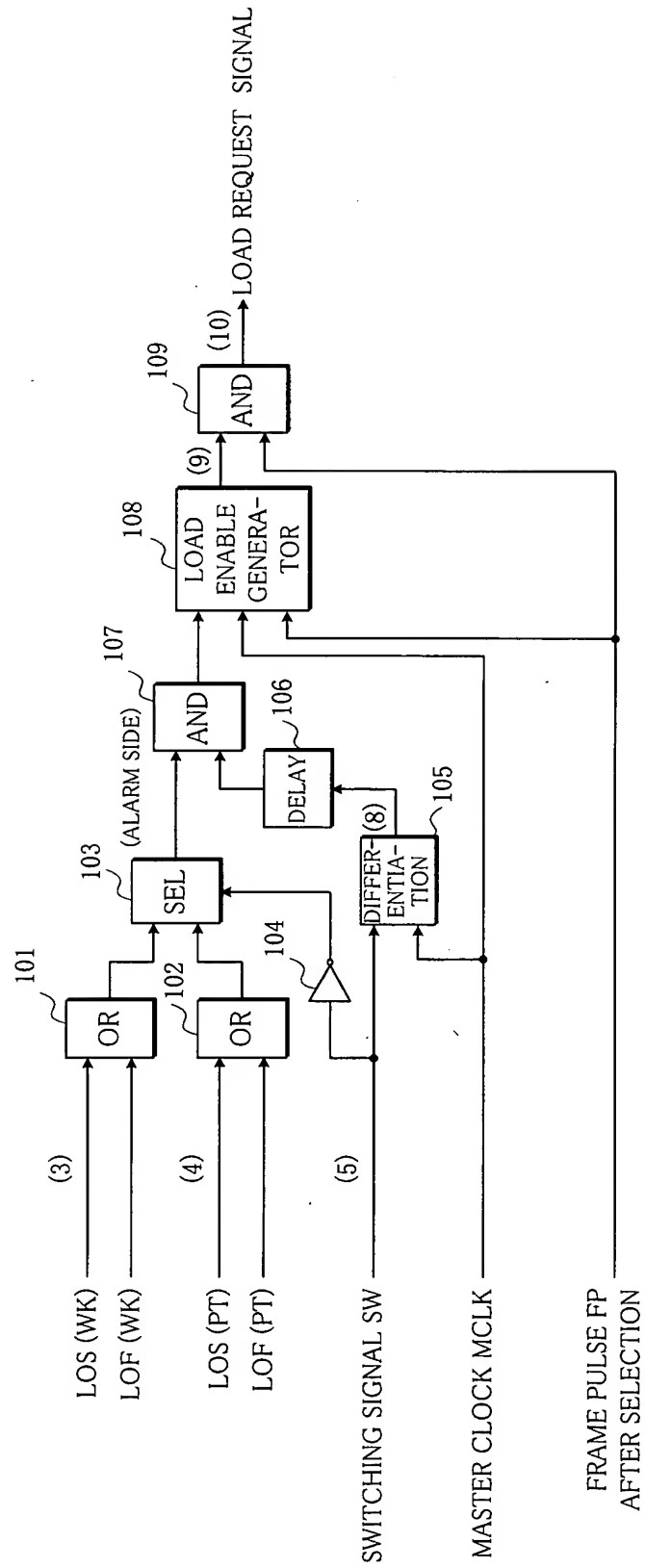


FIG.3A



FIG.3B

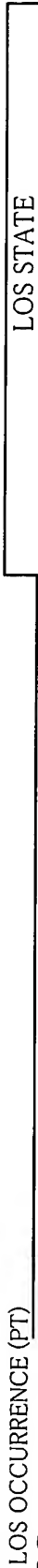


FIG.3C



FIG.3D



FIG.3E



FIG.3F



FIG.3G



FIG.3H



FIG.3I



FIG.3J

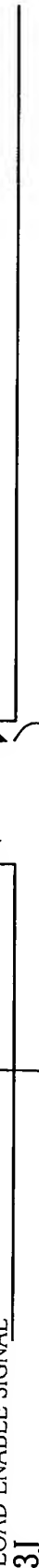


FIG.3K



FIG.3K



FIG.4

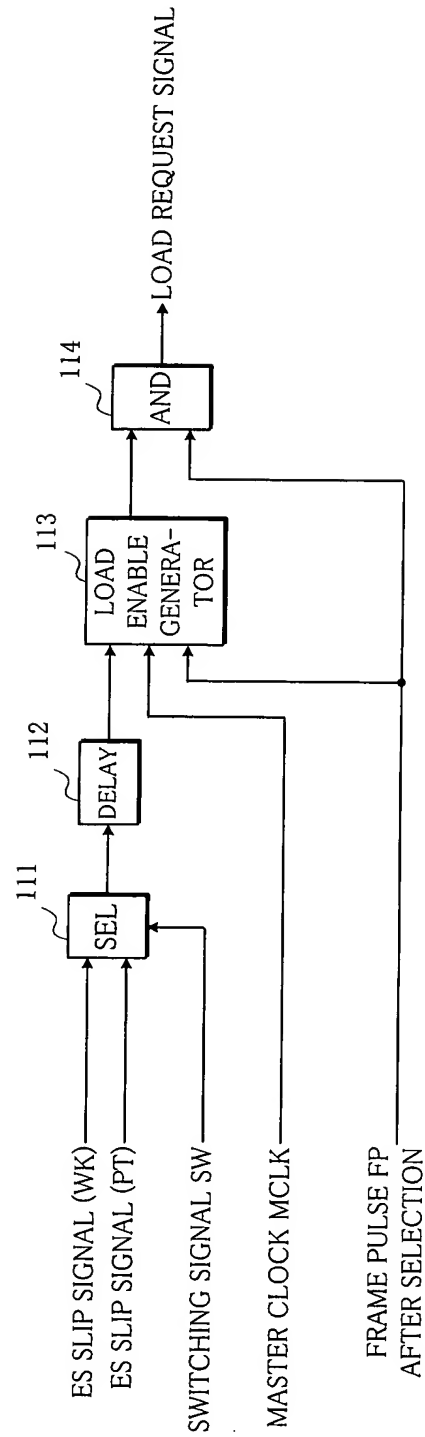


FIG.5

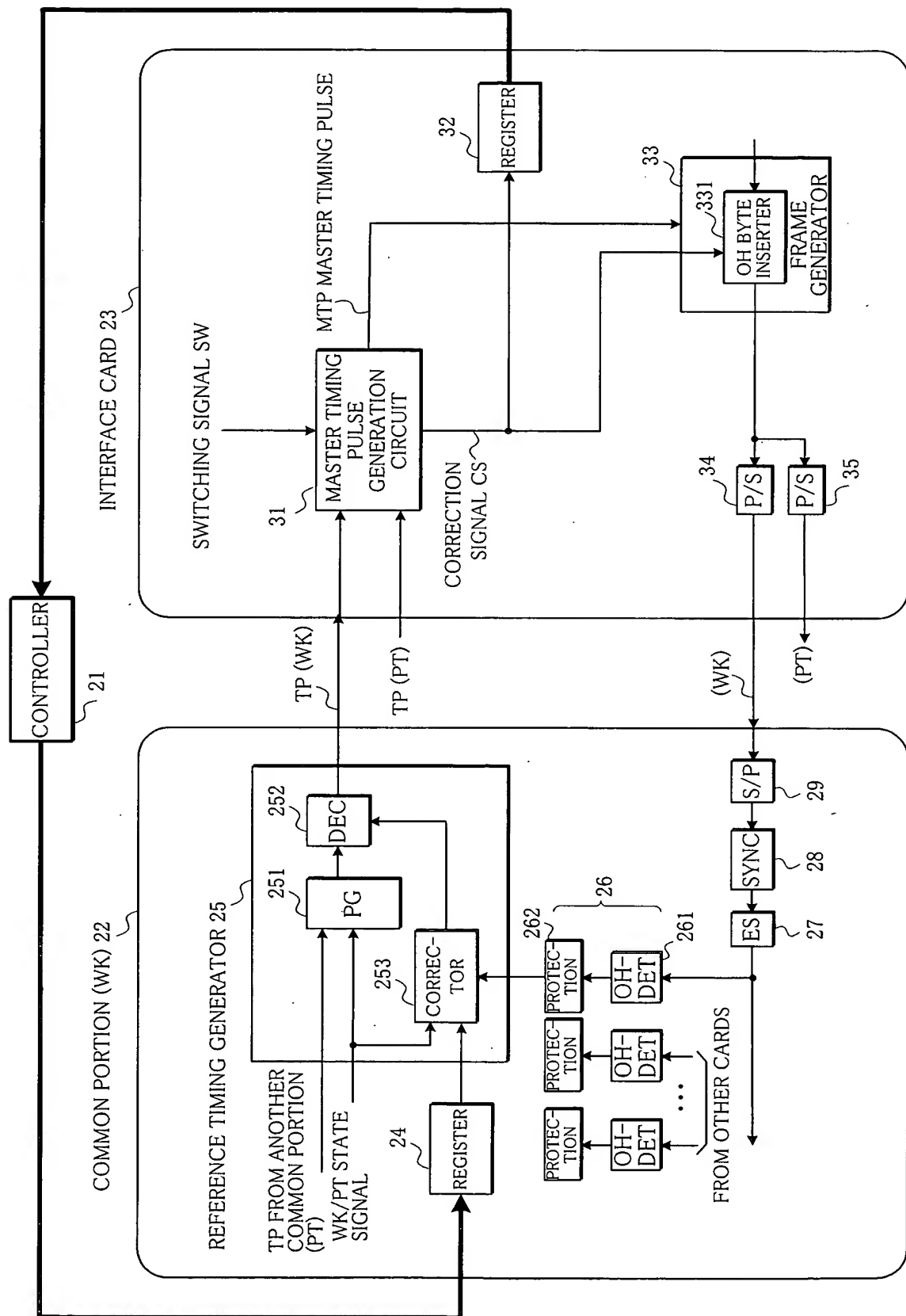


FIG. 6

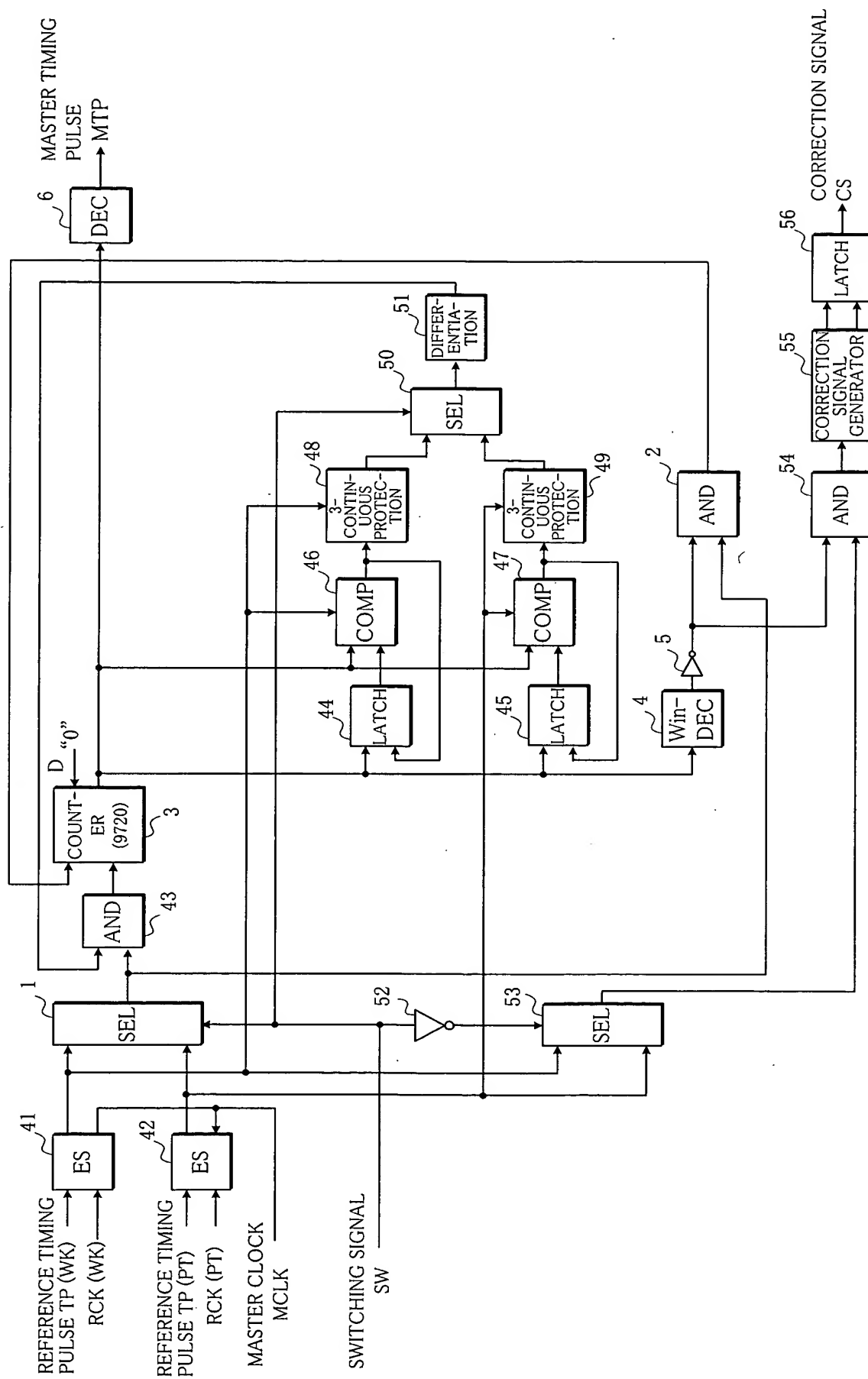


FIG. 7

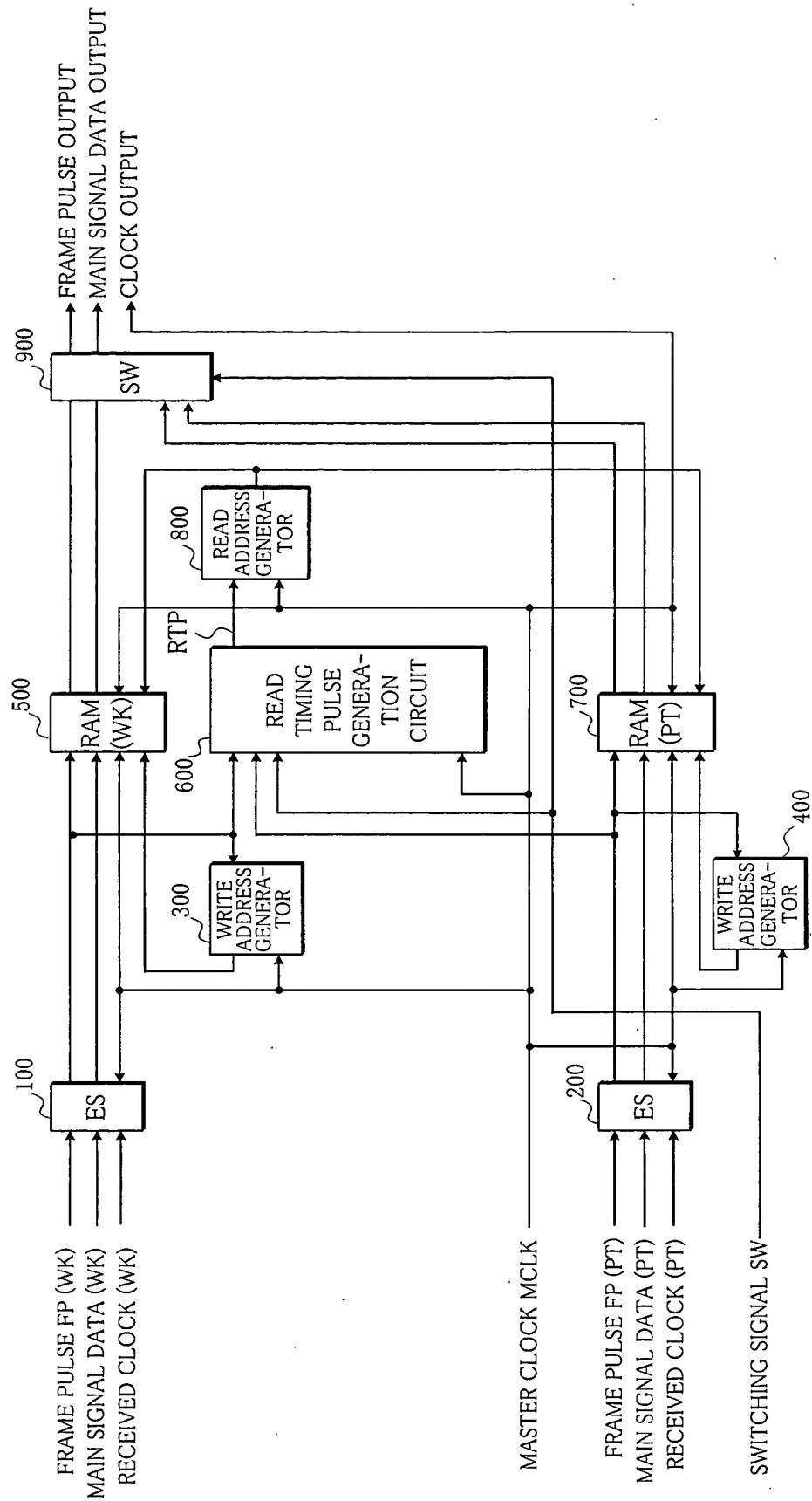
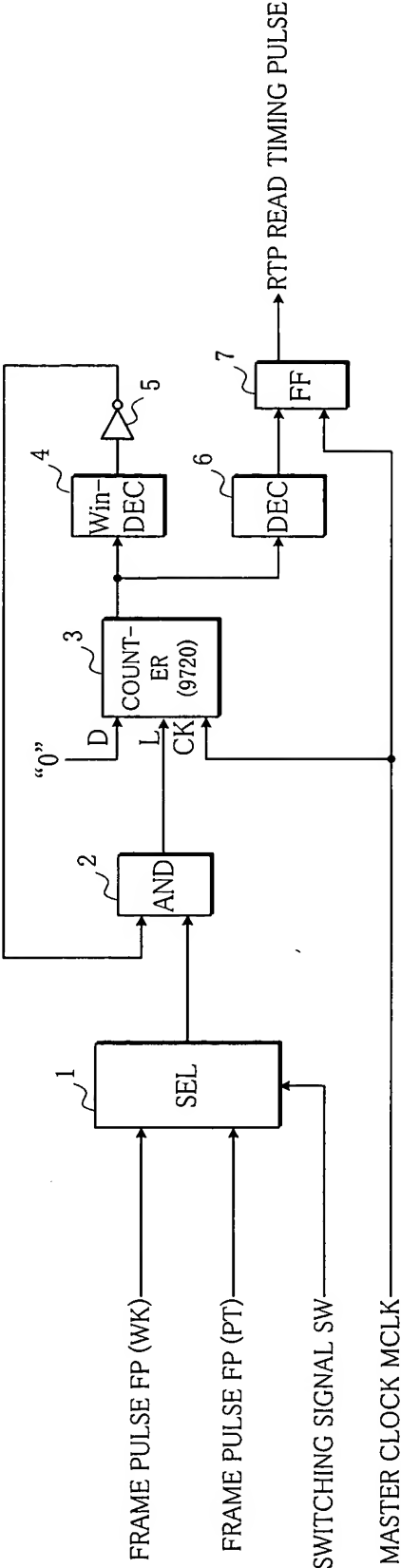


FIG.8  
PRIOR ART





PRIOR ART

FIG.9A

POWER-ON RESET

FIG.9B

FRAME PULSE FP (WK)

FIG.9C

FRAME PULSE FP (PT)

FIG.9D

FRAME PULSE FP

AFTER SELECTION

FIG.9E

COUNTER VALUE

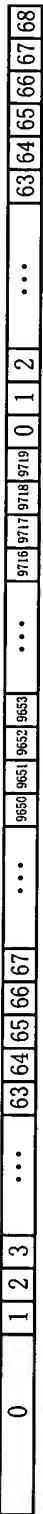


FIG.9F

Win-DEC OUTPUT

FIG.9G

DEC OUTPUT

FIG.9H

READ TIMING PULSE

(FF OUTPUT)

MONITORING WINDOW MW (134 BITS)

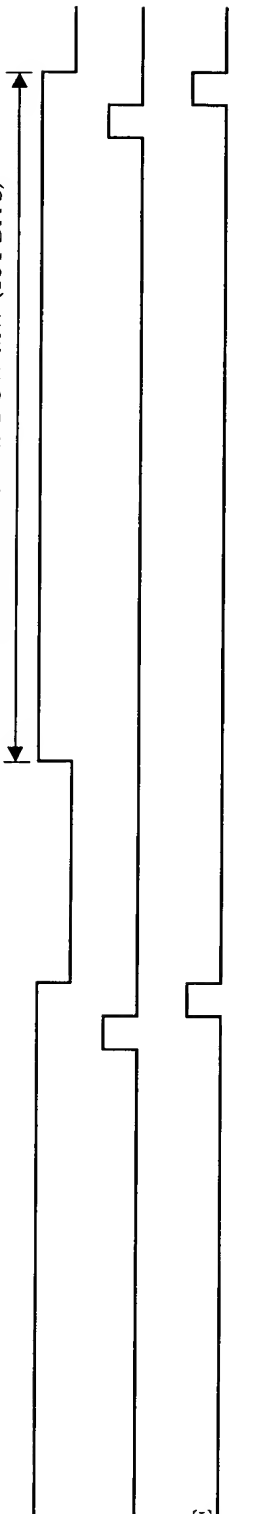
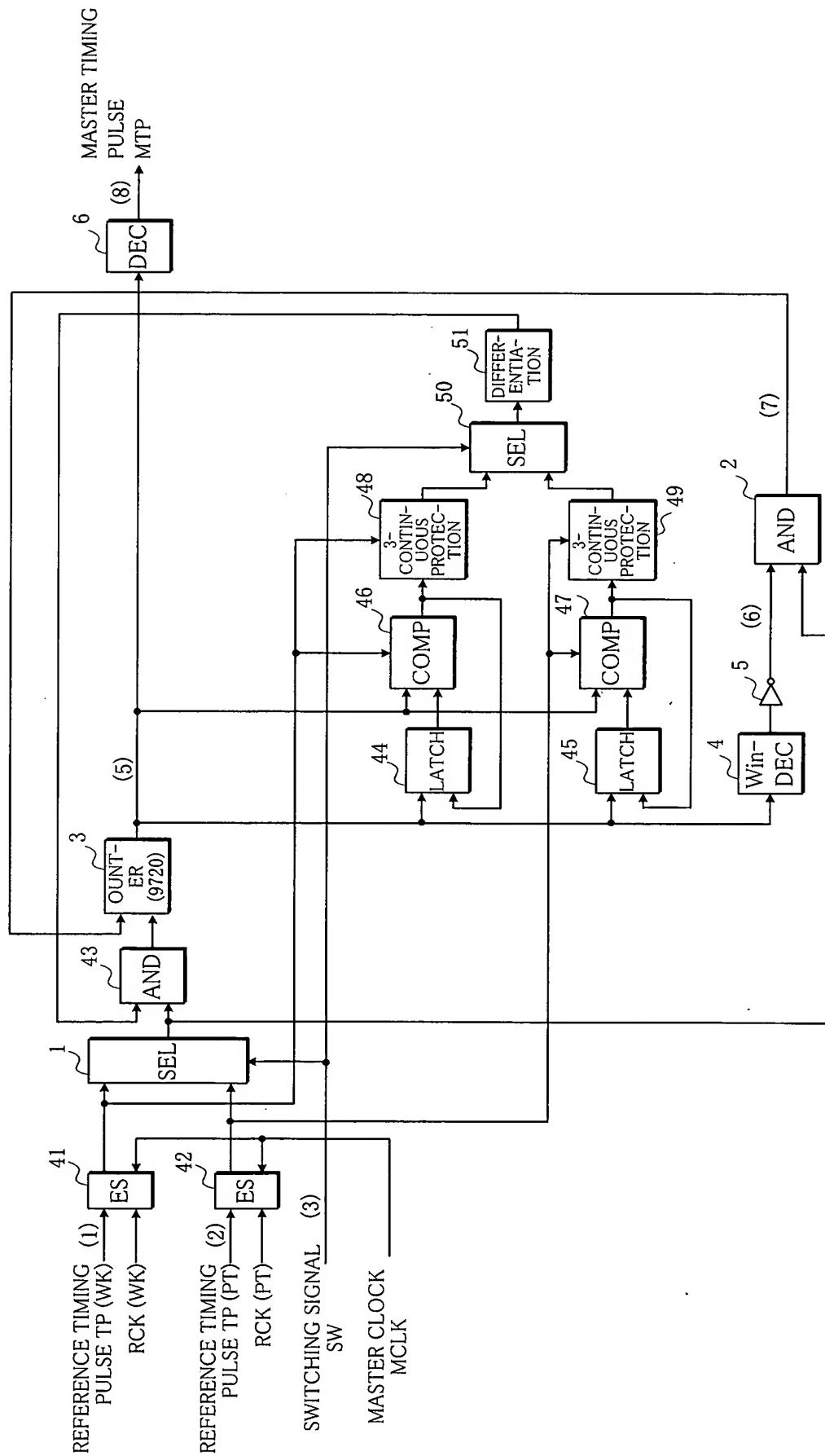


FIG.10  
PRIOR ART



# PRIOR ART

